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a processor for controlling the vehicle control device by using the at least one of a device control program and data stored in the first memory;

wherein the processor is constructed to store in the second memory a rewrite control program sent from an external device for executing rewriting of the first memory upon determination of a rewrite mode based on information sent from the external device, and to rewrite first memory with at least one of a new device control program and data sent from the external device based on the rewrite control program stored in the second memory; and

E1
wherein the processor is further constructed to change a speed of communication with the external device to a new communication speed after the determination of a rewrite mode, the new communication speed being higher than a communication speed of the information sent from the external device for the determination of a rewrite mode.

35. (New) A control unit for a vehicle having a vehicle control device, the control unit comprising:

a first memory storing therein at least one of a device control program and data for controlling the vehicle control device, the first memory being a nonvolatile type;

a second memory provided in addition to the first memory; and

a processor for controlling the vehicle control device by using the at least one of device control program or data stored in the first memory;

21 wherein the processor is constructed to store in a second memory a rewrite control program sent from an external device for executing rewriting of the first memory upon determination of a rewrite mode based on information sent from the external device, and to rewrite the first memory with at least one of a new device control program and data sent from the external device based on the rewrite control program stored in the second memory; and

32 wherein the processor is further constructed to change a speed of communication with the external device to a new communication speed before starting to execute the rewriting of the nonvolatile memory based on the rewrite control program, the new communication speed being higher than a communication speed of the information sent from the external device for the determination of a rewrite mode.

36. (New) The control unit of claim 35, wherein the processor is further constructed to change the speed of communication with the external device to the new communication speed after the determination of a rewrite mode.

37. (New) The control unit of claim 35, wherein the processor is further constructed to change the speed of communication with the external device to the new communication speed before the rewrite control program is sent from the external device.